

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (Use several sheets if necessary) PTO Form 1449	Atty Docket No. 112740-1074	Application No. 10/535212 PCT/EP2004/051332
	Applicant Lungwitz	
	Filing Date	Group 2841

U.S. PATENT DOCUMENTS						
Examiner's Initials	Document Number	Publication Date	Inventor	Class	Subclass	Filing Date If Appropriate
/HB/	5,895,884	4-20-99	Davidson			
/HB/	6,486,534	11-26-02	Sridharan et al.			

FOREIGN PATENT DOCUMENTS							
Examiner's Initials	Document Number	Publication Date	Country	Class	Subclass	Translation	
						Yes	No
/HB/	EP 0 735 811	10-02-96	European				
/HB/	JP 09036512	7-2-97	Japan				
/HB/	JP 10112517	4-28-98	Japan				
/HB/	EP 0 886 464	12-23-98	European				
/HB/	EP 0 977 298	2-2-00	European				
/HB/	DE 199 45 427	11-2-00	Germany				
/HB/	JP 2001237586	8-31-01	Japan				

Examiner's Initials	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)
/HB/	"Printed Circuit Board Pattern Design Method for Low EMI Noise", IBM Technical Disclosure Bulletin, IBM Corp. New York, US BD 37 Nr. 6A, June 1994 (6-1-94) Page 603, XP-000455901

Examiner: /Hung Bui/	Date Considered: 03/27/2007
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	